芝浦工業大學 SHIBAURA INSTITUTE OF TECHNOLOGY

2019 Global PBL (outbound) Program Report

Hardware Development PBL

Date	Place	Partner Organization	Students' Major and Grade	Participants' Information	SIT Instructor
July 31 ～ Augusut 17, 2019	**United States**	**California State University, East Bay**	・Department of Electronic Engineering、Department of Computer Science and Engineering ・Undergraduate 3rd grade、Undergraduate 2nd grade	(SIT) Students 13, TA 3, Professor 2 (California State University, East Bay) Students 4, Professor 1	SASAKI masahiro(Department of Electronic Engineering)、SHIMAZAKI midori(Department of Information Science and Engineering)、USAMI kimiyoshi(Department of Information Science and Engineering)

To acquire practical skills through this program, the participating students have developed a simple electrical signals observation instrument such as an oscilloscope or a logic analyzer utilizing an FPGA (Field Programmable Gate Array) with the other department students at California State University, East Bay. The simple oscilloscope has been realized by controlling a discreate Analog-to-Digital Converter IC, Digital-to-Analog Converter IC, and VGA monitor with the FPGA which is the typical reconfigurable logic device.
This program has been held for 18 days from the end of July to Aug. 17th. There were 7 student participants, 3 teaching assistants and 1 professor from the Dept. of Electronic Engineering and 6 student participants and 1 professor from the Dept. of Computer Science and Engineering. Furthermore, 1 professor and partially 4 student participants from CSUEB.