

AY2024 Global PBL (Inbound) Performance Report

Research-oriented gPBL between SU					
Date	Place	Partner Organization	Students' Major and Grade	Participants' Information	SIT Instructor
2024/06/11 ~2024/06/19	Japan	Silpakom University	*Department of Electronic Engineering, Advanced Electronic Engineering *Undergraduate 3rd grade, Undergraduate 4th grade	(SIT)	ISHIKAWA Hiroyasu(Electrical and Electronic Engineering Advanced Electronic Engineering, UENO Kazuyoshi(Electrical and Electronic Engineering Advanced Electronic Engineering), KOIKE Yoshikazu(Electrical and Electronic Engineering Advanced Electronic Engineering), YOKOI Hideki(Electrical and Electronic Engineering Advanced Electronic Electronic Engineering Advanced Electronic Engineering), YOKOI Hideki(Electrical and Electronic Engineering Advanced Electronic Engineering)



Image1 Field trip

The purpose of this gPBL is to conduct research in the nanoelectronics, photonics field and informatics, bioelectronics field at SIT. The gPBL was held by Silpakorn university (SU) and SIT. This year, four groups were made, which was constructed with two or three SU students and three or four SIT students. In each group, SU and SIT students worked together to get acquainted with current research and to learn various values through joint work with students from other countries. All groups learned about the current state of research fields in the Integrated Photonic Device Laboratory, Semiconductor Electronics Laboratory, Nanoelectronics Laboratory and Electro-mechanical Systems Laboratory. They also practiced basic research routines, discussed future research topics, and delivered a presentation.



Image2 Lecture of Prof.Ploybussara



Image3 Practical training activity 1



Image4 Practical training activity 2



Image5 Final Presentation