

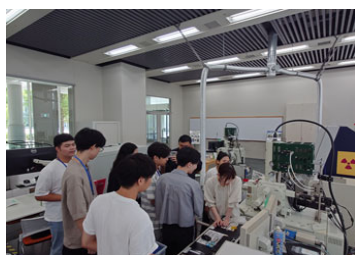
Inbound global internship with KMUTT in the field of devices

Date	Place	Partner Organization	Students' Major and Grade	Participants' Information	SIT Instructor
2024/07/31 ~2024/08/08	Japan	King Mongkut's University of Technology Thonburi	Department of Electronic Engineering, Department of Electrical Engineering, Department of Fundamental Mechanical Engineering, Department of Engineering Science and Mechanics, Advanced Mechanical Engineering, Department of Electronic Engineering Undergraduate 1st~4th grade	(SIT) Students 10, Student Staff 8, Professor 3 (King Mongkut's University of Technology Thonburi) Students 10, Professor 5	YOKOI Hideki (Electrical and Electronic Engineering Advanced Electronic Engineering), UENO Kazuyoshi (Electrical and Electronic Engineering Advanced Electronic Engineering), ISHIKAWA Hiroyasu (Electrical and Electronic Engineering Advanced Electronic Engineering)



Image1 Activities in a laboratory1

In the Semiconductor Electronics Laboratory, Nanoelectronics Laboratory, and Integrated Optical Devices Laboratory within the Physical Properties/Devices field of the Electrical and Electronic Engineering Department of SIT, teams consisting of 3 to 4 SIT students and 3 to 4 KMUTT students worked on their research projects. Following a brief lecture on research activities, the students learned the utilization of facilities and equipment in each laboratory. They engaged in tasks such as thin-film fabrication, optical modeling, thin-film evaluation, simulation, and discussion. During the final presentation, they summarized and shared the outcomes of their research projects. Subsequently, through a question-and-answer session involving participating students and teachers, they analyzed the results and discussions while also exploring future research topics.



Activities in a laboratory2



Activities in a laboratory3



Final presentation